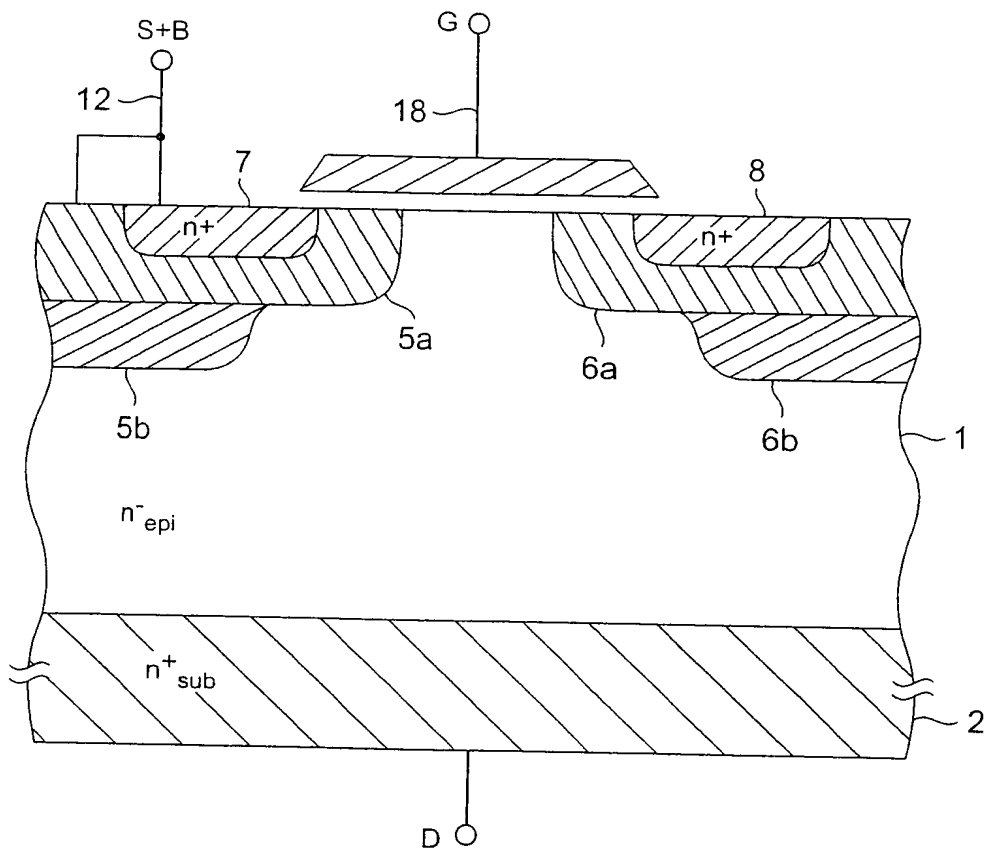




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CONVENTIONAL MOSFET

FIG. 1
(PRIOR ART)



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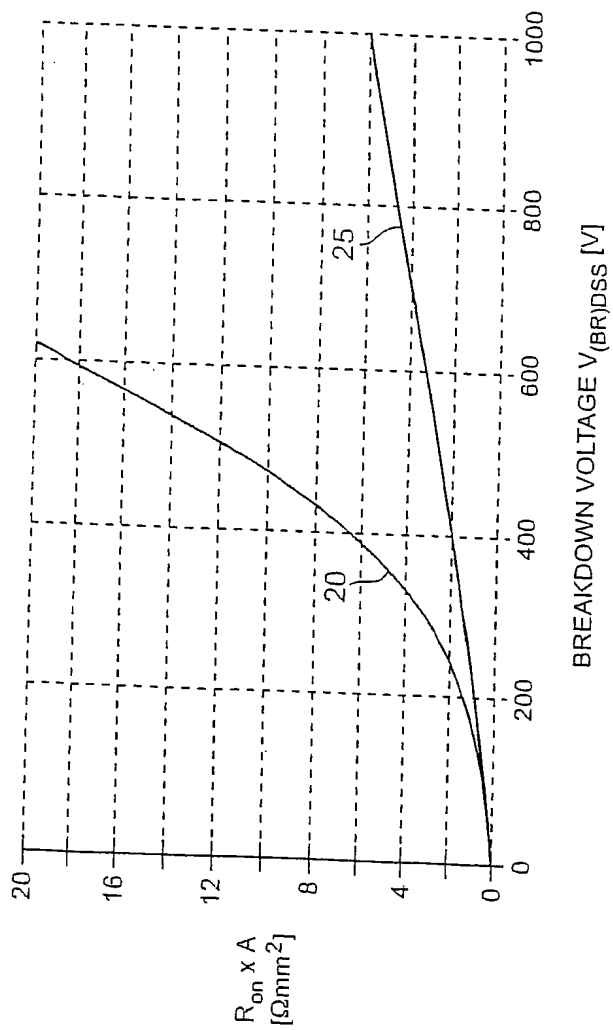
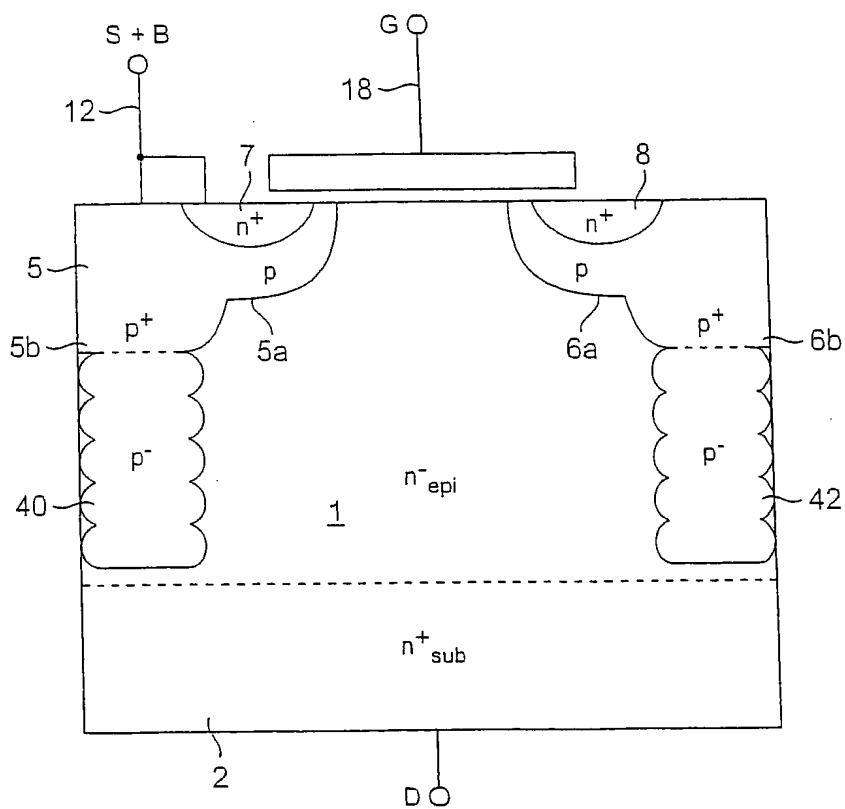


FIG. 2

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THE DOPANT DISTRIBUTION OF A HIGH VOLTAGE VERTICAL
 DMOS TRANSISTOR WITH A RELATIVELY LOW ON-RESISTANCE

FIG. 3

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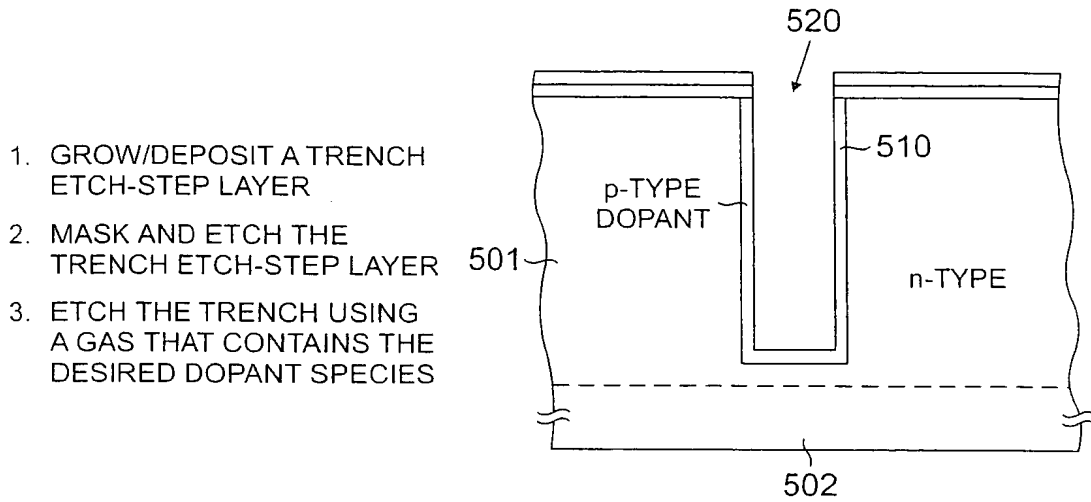


FIG. 4A

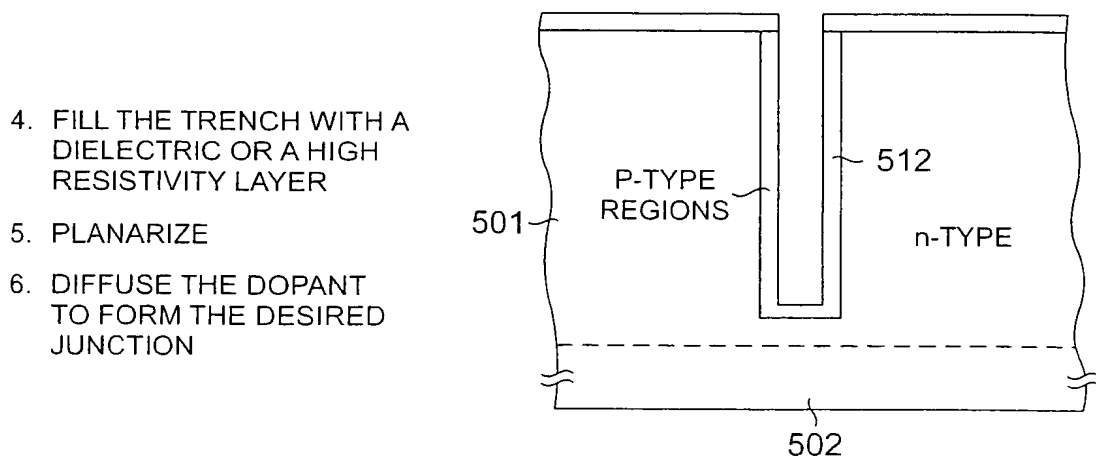
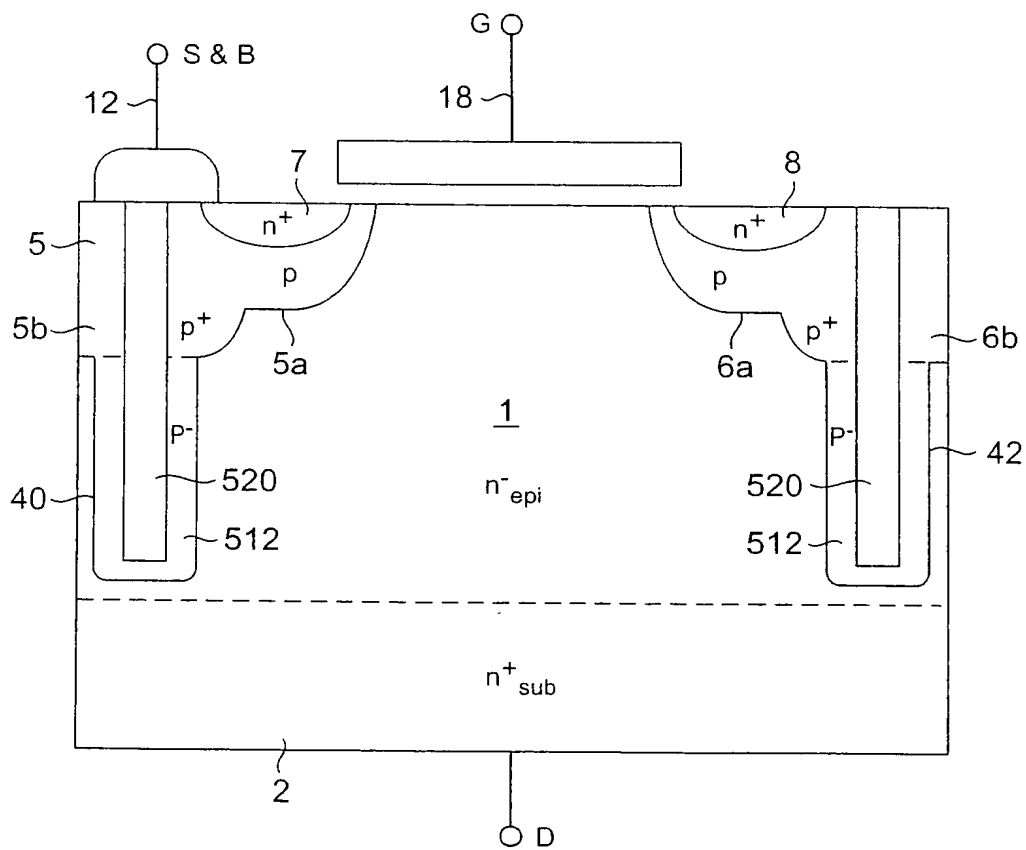


FIG. 4B



THE DOPANT DISTRIBUTION OF A HIGH VOLTAGE VERTICAL
 DMOS TRANSISTOR WITH A RELATIVELY LOW ON-RESISTANCE

FIG. 5